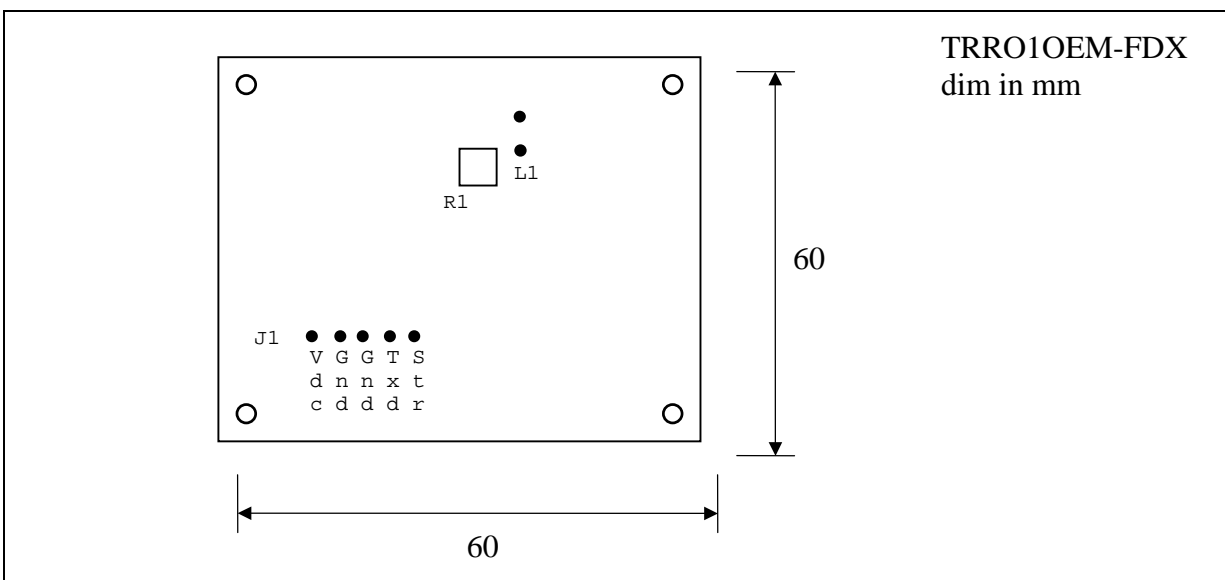


TRRO1OEM-FDX

The TRRO1OEM-FDX is a small, inexpensive and easy to use reader/decoder board for passive RF identification tags. It can read ISO 11785/11784 FDX type tags. It is intended for OEM applications. A power source and an antenna is all that is required to use the reader. Its low-power requirements permit battery operation and can be integrated into hand held devices.



Specifications

Function	Reader/decoder for 134.2 kHz AM-PSK DBP ISO 11784/85 RF/32 128 bit RF identification tags
Interface	RS232, 9600 baud, 8 data bits, no parity, 1 stop bit
Antenna type	1.5 mH coil (calculated, see note)
Read range	Antenna and tag dependent, practical maximum about 70 cm
Power requirements	9 - 12 VDC regulated, 60 mA minimum
Dimensions	60 mm x 60 mm x 15 mm (2.375 inches x 2.375 inches x 0.625 inches)
Operating temperature	Tbd
Humidity	non-condensing
Connections	Standard: 4-5 pin .1 header (J1), 2 pin .1 header (L1) <i>see ordering info</i>

TRRO1OEM-FDX *continued*

General Description

The TRRO1OEM-FDX board performs all the functions necessary for a RFID reading station. It continuously powers, reads and decodes transponders that are within its reading range. When a transponder tag passes within range of the reader antenna, the RF magnetic field generated by the reader powers the tag. The tag then transmits its data. The reader board demodulates and decodes the data. The data is then sent as a packet using a two-wire RS232 interface. While the tag remains within reading range it will be continuously powered and the reader will continuously transmit its data.

Connections

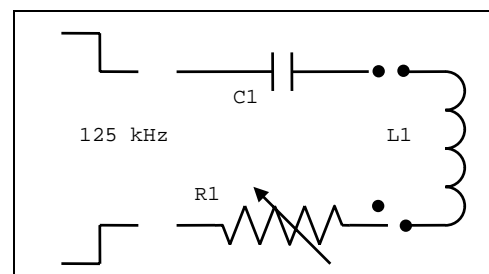
The standard connector J1 is a 4 (or 5 pin) 0.100 inch spacing single row male header.

1. **+VDC**: Connect to positive side of power supply. A 9 to 12 VDC regulated supply is required. A 9 volt battery can also be used (positive + terminal). Although the board offers some protection, be sure to check for proper polarity.
2. **GND**: Connect to ground (-) side of power supply.
3. **GND**: Connect to common ground of RS232 receiver.
4. **TXD**: Connect to RXD (receive) of RS232 terminal. It is used to serially transmit the data packet.
5. **STR**: The STR signal is a +5VDC TTL level (positive logic). It can be used as an interrupt signal to warn the terminal device that a data packet is about to be sent. A LED is normally soldered on board, using this signal, to give a visual indication of reads.

The standard connector L1 is a 2 pin 0.200 inch spacing single row male header. It is used to connect the antenna.

Antenna Design

A single antenna is required for powering and reading the transponders. The antenna is used in a series resonant circuit, formed by C1, L1, and R1. The board requires, based on calculation with the on-board 1nF capacitor, an antenna with an inductance of 1.5 milliHenries. Proper value and shape of the antenna are installation dependent and must be determined by the user. The nominal resonant frequency is 134.2 kHz. To improve the reading range, the board will automatically adjust the driver's frequency to the resonance of the antenna circuit. The antenna can be a simple air wound coil. The reading range is spherical with the antenna located at the equator. Minimum and maximum read range is determined, in great part by the size of both the reader and tag antennas



Read Range Adjustments

The read range can be adjusted using the trimmer potentiometer, R1. This changes the Quality factor of the series resonant circuit. Although increasing the Q factor of the circuit increases the read range, it also increases the amount of spurious signal received. Therefore the boards should be individually adjusted with each attached antenna in their final environment.

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Specifications and functionality are subject to change without notice

TRRO1OEM-FDX *continued*

Data Transmission *firmware version 1.3 and greater*

The information is sent using a 2 wire (txd and gnd) RS232 interface. It operates at 9600 baud 8N1. The data in a ISO FDX tag is 104 bits long. The data packet is comprised of 1 start byte, 16 data byte in ASCII, and one stop byte.

The start byte is always a ':' (58d, 3Ah).

The 16 data byte are an ASCII representation of the 32 sixteen hexadecimal serial code digits (5 numbers) stored in the tag that has just been read.

The stop byte is always an ASCII 'carriage return' (13d, 0Dh).

Data packet is:

: nn nn nn nn nn cccc ee rr rr aa ss ss xx xx xx [0D]

Where:

nn: National Identification Code	38 bits (5 bytes)
cc: Country Code	10 bits (2 bytes)
ee: Extension Block Flag	1 bit (1 byte)
rr: Reserved	14 bits (2 bytes)
aa: Animal flag	1 bit (1 byte)
ss: CRC16 CCIT Checksum	16 bits (2 bytes)
xx: Extension Data	24 bits (3 bytes)

The 16 data byte are sent as ASCII representation: 32 digits: '0'-'9', 'A'-'F' (30h-39h, 41h-46h)

Example:

:00499602D2026C0000000122C6000000cr

National ID Code:	1234567890 (animal number, 499602D2h)
Country Code (ISO 3166):	620 (Portugal, 26Ch)
Data Block Flag:	0 (No Extended data, 0h)
Reserved field:	0 (No reserved data, 0h)
Animal Flag:	1 (Yes this is an animal ID, 1h)
CRC (per Annex B):	8902 (Calculated on 64 bits above, 22C6h)
Extension Data:	0 (No extended data, 0h)

The data packet is sent every time a tag is detected. If a tag remains in the reading field, its data will be sent continuously.

The STR line can be used to signal to the receiving terminal that data is about to be sent. The STR line is raised (high logic level, +5VDC) for about 1 milliseconds before the data packet is sent. The STR line remains high for the duration of the transmission of the data packet. It is brought down (low logic level, 0 VDC) after the last bit is sent. This signal could be used to interrupt a microcontroller, which would then gather the bytes of incoming data.

Ordering information: TRRO1OEM-FDX-J4-L1-LED (*this is our standard board*).

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